

# Rust for Embedded Systems: Current State and Open Problems

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## Abstract

Embedded software is used in safety-critical systems such as medical devices and autonomous vehicles, where software defects, including security vulnerabilities, have severe consequences. Most embedded codebases are developed in unsafe languages, specifically C/C++, and are riddled with memory safety vulnerabilities. To prevent such vulnerabilities, RUST, a performant memory-safe systems language, provides an optimal choice for developing embedded software. RUST interoperability enables developing RUST applications on top of existing C codebases. Despite this, even the most resourceful organizations continue to develop embedded software in C/C++.

This paper performs the first systematic study to holistically understand the current state and challenges of using RUST for embedded systems. Our study is organized across three research questions. We collected a dataset of 6,408 RUST embedded software spanning various categories and 6 Static Application Security Testing (SAST) tools. We performed a systematic analysis of our dataset and surveys with 225 developers to investigate our research questions. We found that existing RUST software support is inadequate, SAST tools cannot handle certain features of RUST embedded software, resulting in failures, and the prevalence of advanced types in existing RUST software makes it challenging to engineer interoperable code. In addition, we found various challenges faced by developers in using RUST for embedded systems development.

## CCS Concepts

• **Security and privacy** → **Embedded systems security**; • **Computer systems organization** → **Embedded software**; *Real-time operating systems*; • **Hardware** → Safety critical systems.

## Keywords

Rust, Deep Embedded Systems, Security

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## 1 Introduction

Our dependence on embedded devices (*e.g.*, IoT devices), has significantly increased, controlling various aspects of our daily lives, including homes [11], transportation [9], traffic management [130], and the distribution of vital resources like food [102] and power [97]. The adoption of these devices has seen rapid and extensive growth, with an estimated count of over 50 billion devices [8]. Vulnerabilities in these devices have far-reaching consequences [4, 14] due to the pervasive and interconnected nature of these devices, as exemplified by the infamous Mirai botnet [83].

Most embedded software are developed in “unsafe” (*i.e.*, not memory-safe) languages, specifically C/C++, because of the low memory footprint, good performance, and the availability of extensive support software. It is well-known that software developed in unsafe languages is prone to security vulnerabilities, especially memory safety vulnerabilities [25, 89, 135]. Likewise, embedded systems are riddled with security vulnerabilities [7, 13, 62, 136]. The most recent URGENT/11 [15] vulnerabilities in VxWorks that affected millions of medical [56], SCADA systems [142], and industrial controllers [22] are all because of memory safety (spatial) violations. It is important to ensure that embedded systems do not contain memory-safety issues. Automated memory-safety retrofitting techniques [32, 48, 64, 92, 93, 131] based on compile-time instrumentation add significant overhead (both space and runtime) and are inapplicable to resource-constrained embedded systems.

Our analysis (details in Our Extended Report [126]) of security vulnerabilities in various Real Time Operating Systems (RTOSes) (an important class of embedded software) for the past ten years shows that 59 (54.2%) of them are memory corruption vulnerabilities, *i.e.*, spatial or temporal memory issues. It is important to use memory-safe languages to prevent such vulnerabilities. Furthermore, recently, the White House released a report [140] requiring future software to be developed in memory-safe languages. Traditional memory-safe languages, such as Java, have high overhead

and are not suitable for embedded systems. RUST [121] is a memory-safe language that is shown to have comparable performance as native code. Furthermore, RUST can easily interoperate with existing unsafe codebases [61], enabling incremental adoption. RUST team has a special focus on embedded systems [112], and several works [74, 75] demonstrate the feasibility of engineering a complete embedded software stack in RUST. Furthermore, RUST popularity is rising [101], and it is now adopted in Linux kernel [79] and Android [108]. Unfortunately, embedded systems are continuing to be developed in C. Even the most resourceful organizations, such as Microsoft, continue to develop embedded systems in C, as demonstrated by their recent Azure RTOS [18]. Previous works [55, 145] investigated the challenges of adopting RUST for regular software. However, no work tries to understand factors affecting the use of RUST for embedded systems development.

In this paper, we perform the first systematic study to holistically understand the issues in using RUST for developing embedded systems. Specifically, we explored the following research questions:

- **RQ1: Software Support.** How effective (quantity and quality) is the existing RUST software support for embedded system development?
- **RQ2: Interoperability of RUST.** Given that most of the existing embedded systems are in C, how well can RUST interoperate with existing C codebases? and what are the challenges specific to embedded codebases?
- **RQ3: Developers Perspective.** What challenges do developers face in using RUST for embedded system development?

We collected a dataset of 6,408 RUST embedded software packages (or crates) spanning various categories and 6 SAST tools. We performed a systematic analysis of our dataset and surveys with 225 developers to investigate our research questions. Our study revealed several interesting findings (16), drawbacks of existing tools on embedded crates, and open problems (8) to increase the adoption of RUST for embedded systems. A few interesting findings include the following: Embedded crates use more (~2X) **unsafe** blocks than non-embedded crates, significantly increasing the possibility of memory safety issues. However, existing techniques to isolate **unsafe** blocks are not applicable to embedded systems. Existing developer support tools related to RUST, such as `c2rust`, fail on majority of embedded codebases, as these tools fail to handle the diverse build systems and execution semantics of embedded systems. The state-of-the-art RUST SAST tools perform poorly on embedded crates. The superior type-system of RUST makes it challenging to engineer interoperable embedded systems code. Our observations are in line with the developer survey, and many developers consider the RUST documentation for embedded systems poorly organized and want the documentation to contain more examples. In summary, the following are our contributions:

- **Software Study:** We perform a systematic study of the RUST software ecosystem to support the use of RUST for embedded applications and highlight opportunity areas for adoption.
- **Tool Study:** We systematically studied the effectiveness of various (9) RUST related tools, *i.e.*, SAST tools, quality checking, and conversion tools, on embedded crates and identified various weaknesses specific to embedded systems.

- **Developer Aspects:** We performed a large-scale developer survey (with 225 developers) that highlights the challenges for slow adoption of RUST for embedded applications.
- **Dataset, Findings, and Open Problems**<sup>1</sup>: We curated a set of 6,408 embedded RUST crates cataloged into various categories along with the necessary infrastructure to run analysis tools. Our findings shed light on challenges in adopting RUST for embedded systems, insights into open problems, and possible research directions.

## 2 Background

This section provides the necessary background information for the rest of our work.

### 2.1 Embedded Systems

Embedded systems are designed to perform a designated set of tasks in a resource-constrained environment and on battery-powered devices. There are several ways to categorize embedded systems. Previous work [91] categorizes embedded systems based on underlying Operating System (OS).

(a) Type-1 systems have feature-rich general-purpose OSes retrofitted for embedded systems.

(b) Type-2 systems or constrained devices [26] use specialized embedded OSes, which are usually designed as Real Time Operating System (RTOS), *e.g.*, WEMO Light controller [138] running FreeRTOS [52].

(c) Type-3 systems do not use OS abstractions and are rarely used in commercial products.

Previous work [11] shows consumer IoT devices, such as door knobs and temperature controllers, are mostly Type-2, which we primarily focus on. Type-2 systems execute on battery-powered and resource-constrained Microcontroller Units (MCUs). These systems have a lot of diversity in terms of hardware (MCU and peripherals) and supported software [91, 139]. For instance, there are 31 different RTOSes [98]. To handle this diversity, Type-2 systems have a layered design [128] (illustrated in our Extended Report [126]). Application logic is implemented in tasks managed by an RTOS.

**Execution Semantics.** The application and all the layers are compiled into a single monolithic binary and flashed onto the on-chip flash memory. On reset, the contents of the memory are loaded into RAM, and execution starts from a pre-defined address, *i.e.*, start or reset address. The tasks get scheduled per the scheduling policy, and handlers get triggered on corresponding events.

### 2.2 RUST

RUST is a programming language created by Mozilla to build efficient and safe low-level software [27, 68, 71, 121, 124]. RUST is targeted to achieve performance comparable to programs written in C while avoiding many safety issues in C, including concurrency and memory safety bugs. This section provides a brief overview of RUST's safety features. We recommend the RUST's official book [121] for a comprehensive understanding of these features.

<sup>1</sup><https://zenodo.org/records/12775715>

**Features and Safety Guarantees.** RUST has several features, such as scopes, borrowing rules [120], single ownership [118], and lifetimes [115], which force developers to follow certain practices enabling verification of memory safety properties (mostly) at compile time. For instance, all read-write variables should be explicitly marked as mutable (*i.e.*, `mut`). RUST provides both spatial and temporal memory safety. We provide a discussion of these guarantees in Our Extended Report [126]

**Unsafe Rust.** RUST features can be too restrictive in a few cases. For instance, RUST requires all global variables to be read-only, *i.e.*, disallows `mut`. Similarly, we may need to call a C library function, which is also not allowed. RUST provides `unsafe` blocks [119] to relax these restrictions and enable interaction with external language (or foreign) functions. Arbitrary regions of code can be enclosed in an `unsafe` directive, and such code will be permitted certain (otherwise disallowed) actions, such as modifying a mutable global variable, dereferencing a raw pointer, calling an unsafe or external method, etc.

**Foreign Function Interface (FFI) Support.** RUST supports easy interaction with functions written in foreign languages through its Foreign Function Interface (FFI) [61]. Specifically, such functions need to be annotated with special attributes, which enables the RUST compiler to generate appropriate code respecting the ABI of the target language.

**Build System and Package Management.** RUST uses an integrated and easy-to-use build system and package manager called `Cargo` [110], which downloads library packages, called crates, as needed, during builds. Developers specify the build configuration along with all dependencies in a `.toml` file [133] – an organized key-value text file. Rust has an official community package registry called crates.io [36], which (as of 29 April 2024) has more than 144K crates (*i.e.*, libraries) – a 200% increase over the last two years.

**RUST Compilation Attributes.** RUST supports attributes or configurations that enable compilation specialization. These attributes can be at various levels, *e.g.*, crate level, file level, function level, etc. `no_std` attribute [46] is a crate-level attribute that avoids linking the entire standard module and results in small binaries. Embedded software in RUST should use this attribute to produce a self-contained binary independent of OS abstractions. A `no_std` compatible crate should also have all its dependencies to be `no_std` compatible too.

### 3 Study Methodology

Our study aims to perform a holistic analysis to understand various aspects regarding usage of RUST for embedded systems. We aim to answer the following research questions:

- **RQ1: Software Support (§4):** How good is the software support for developing RUST based embedded systems?
- **RQ2: Interoperability (§5):** How effective is the interoperability support of RUST to deal with existing C based embedded codebases?
- **RQ3: Developers Perspective (§6):** What is developers' perspective on using RUST for embedded systems?

### 3.1 Embedded Software Dataset

Our goal is to collect RUST crates that are applicable to embedded systems, *i.e.*, `no_std` compatible, and can be built using one of the embedded toolchains. We also want to identify the necessary compilation steps for all the collected crates.

**3.1.1 Crates collection.** As mentioned in §2.2, crates.io is the official repository for all RUST crates (*i.e.*, libraries). However, there are other well-known sources, such as RUST-embedded project [113] and arewertosyet.com, that also contain embedded RUST projects. We used a two-pronged approach to collect our embedded RUST dataset.

- **Crawling crates.io:** We crawled crates.io (in Feb 2024) and got all the crates that are `no_std` compatible. This is not trivial as crates can declare `no_std` compatibility at various levels. For instance, `arduino_nano_connect v0.6.0` [34] crate declares `no_std` compatibility at the crate level (*i.e.*, in `lib.rs` file). In contrast, `futures-executor v0.3.30` [39] crate uses `cfg` attribute to have only selected code blocks compile for `no_std` environment. We perform lightweight static analysis to identify all such crates.
- **Well-known Sources:** We collected additional crates by crawling other well-known sources, specifically RUST-embedded project and arewertosyet.com.

After deduplication, we collected 11,002 unique crates.

**3.1.2 Identifying Stable Crates.** We tried to build crates using a stable version of RUST and the corresponding compiler. However, we identified that 2,025 (18.4%) embedded crates depend on unstable RUST versions, specifically nightly versions [2]. These versions contain unstable RUST features and might pose threats to the security guarantees of RUST. This is also reflected in one of the concerns (in §6.3) raised by developers in using RUST for embedded systems. We only considered those that build on the stable version of RUST, specifically 1.77.2. This resulted in 8,977 crates.

**3.1.3 Compilation Validation.** The `no_std` compatibility alone is a necessary condition but *not sufficient* for a crate to be usable on embedded systems. For instance, `oc-wasm-futures` [41] crate is `no_std` compatible but is for WebAssembly target, which is not an embedded architecture.

In this step, our goal is to validate crates to check for their applicability to embedded targets and identify the corresponding build commands.

**Identifying Build Command:** All crates can be built using `cargo build`, which uses the default configuration specified in the crate's `cargo.toml` file. However, not all crates have their default configuration to be `no_std`, *i.e.*, the default build step (`cargo build`) may not build `no_std` compatible version. Such crates require special configuration flags to be passed to the build command, *e.g.*, we need to use

```
cargo build --no-default-features --features no_std
```

to build `no_std` variant of `async_cell`. Developers specify such flags through RUST's conditional compilation attributes [1] (`cfg_attr`) as a propositional logic formula. For instance, `#![cfg_attr(all(feature = "no_std", not(feature = "std")), no_std)]` (in `resize v0.8.4` crate) indicates that we need to pass `no_std` flag and not pass `std` to build for `no_std`.

We use a lightweight static analysis technique to identify the appropriate build command. First, we identify all `cfg_attr` directly corresponding to `no_std` (i.e., containing `#![cfg_attr(..., no_std)]`). Second, we analyze the propositional formula to identify the flags that must be enabled or disabled for `no_std`. Our technique was able to find the build commands for 8,148 (90.77%) of crates.

The rest 9.23% failed because of the following reasons: (i) *Incorrect attributes*: Here, crates have an incorrect `cfg_attr` specification. For instance, `zero-crypto v0.1.11` crate has wrong flag name (`feature = "std"`) (Correct: (`feature = "std"`)); (ii) *Incorrect dependencies*: As mentioned in §2, for a crate to be `no_std` compatible all its dependencies should also be `no_std`. However, few crates use dependencies that are either not `no_std` compatible or incorrectly configured. For instance, `linux-kvm v0.2.0` crate depends on `linux-io v0.6.0` crate, which is not `no_std` compatible.; (iii) *Complex attributes specification*: In our analysis, we consider only directly related flags, i.e., those specified along with `no_std` in `cfg_attr`. However, there could be conditional compilation flags that are indirectly related. For instance, `ab_glyph v0.2.23` crate requires `--features="libm"` flag. As we explain in Our Extended Report [126], this flag dependency is specified indirectly and accurately identifying such flags is a combinatorial problem [6].

**Open Problem P0.1:** We need techniques to automatically identify embedded system specific (i.e., `no_std` compatible) build configurations for RUST crates — this also enables identifying mistakes in build configurations (a prevalent problem). One possible approach is to map the dependencies into a boolean formula for constraint solver and use the solution to derive the appropriate flags.

**3.1.4 Embedded Targets Filtering.** There are 23 embedded targets (85 total targets) supported by the latest stable version of `rustc` (version: 1.77.2). For all the crates for which we identified the build commands, we further filtered out crates that did not build for any of the embedded targets. For instance, the `no_std` variant of `winapi v0.3.9` crate is excluded because it requires an underlying operating system environment, which is not present in embedded targets. This resulted in a total of 6,408 crates after filtering out 2,569 crates. Although our study focuses on type-2 systems, *our crates are not exclusively type-2*. For instance, type-1 systems exist for `aarch64-cpu`, one of our targets.

**3.1.5 Categorization.** Based on the functionality, we categorize each embedded crate into eight categories (Tbl. 1). We will present details of these categories in §4.1. We created a Multi-class Random Forest (MCRF) classifier [28] to categorize a given crate. We manually categorized 2000 crates into various categories. Using this as ground truth, we created an MCRF classifier with an F1-score of 82%. We used our MCRF classifier to categorize the rest of the crates.

**Summary.** We collected a total of 6,408 embedded crates along with appropriate build commands that produce `no_std` compatible binary.

## 4 RQ1: Software Support

In this research question, we want to assess the existing software support for engineering embedded systems in RUST. We plan to

investigate the categories of support software that aid in common software engineering activities. Specifically:

- **(For Development) Libraries and Support Software:** As explained in §2, applications in embedded systems are developed atop an RTOS and need necessary libraries that enable communicating with peripherals and provide certain common functionality (e.g., network protocols).
- **(For Testing) SAST Tools:** These are an integral part of software development [107]. We need to have effective SAST tools to ensure the quality of newly developed RUST based embedded systems.
- **(To Handle Existing Codebases) C to RUST Conversion Tools:** Given that most existing embedded codebases are in C, we should have tools to convert C to RUST effectively.

### 4.1 Libraries and Support Software

The available software support, i.e., crates, can be broadly categorized into *hardware support crates* and *utility crates*.

**Table 1: Categorization of all available embedded crates.**

Category Abbr	Type	Crates	
		Total	Wrapper Crates (%Total)
RTOS	RTOS Crates	6	2 (33.33%)
DR	Driver Crates	466	31 (6.65%)
HAL	HAL Crates	57	29 (50.88%)
BSP	Board Support Package	114	100 (87.72%)
PAC	Peripheral Access Crates	565	439 (77.70%)
ARCH	Architecture Support Crates	15	9 (60.00%)
UTIL	Utility Crates	4,764	173 (3.63%)
UNCAT	Others	421	30 (7.18%)
<b>TOTAL</b>		6,408	813 (12.69%)

**4.1.1 Hardware Support Crates.** These provide software abstraction to interact with the hardware i.e., MCU, Peripherals, etc. There are at least 43 different MCU families with various peripherals [5, 90, 132]. We use the following categories to further categorize based on the type of interactions the crates provide. Tbl. 1 shows the summary of different categories of crates available for embedded systems development.

- **Architecture Support:** These help in interacting with the processor and are Instruction Set Architecture (ISA) dependent. For instance, the `aarch64-cpu` crate [111] provides the function `SPSR_EL2.write` to write to the Saved Program Status Register (SPSR) at EL2 exception level on aarch64 processors. *For embedded processors (i.e., Reduced Instruction Set Computer (RISC) ISAs), there are support crates for ARM, MIPS, and RISC-V.*
- **Peripheral Access:** These provide necessary functions to access peripherals on different MCUs. *Out of 43 different MCU families, peripheral crates are currently available for only 16 (37%).* Most of these crates are generated using `svd2rust` utility [42], which automatically converts CMSIS-SVD [63] file (XML description of ARM Cortex-M processors) into RUST structs. Consequently,



most of these crates are for ARM Cortex-M family processors. However, other MCUs, such as AVR-based ATMEGA48PB, do not have SVD files but rather have .atdf files. There exist crates such as atdf2svd [35] to convert these into CMSIS-SVD format, but these tools are not robust and have issues.

**Open Problem P1.1:** We need effective techniques to automatically generate peripheral access crates for non-ARM architectures. Recent advancements in LLM-assisted techniques [143] show promise in solving this problem.

- **HAL Implementation (HAL):** These are implementations of embedded-hal [38], a common Hardware Abstraction Interface for various MCUs. These provide higher-level functions than peripheral crates, which just provide structures encapsulating peripheral registers. For example, `GPIO::write` is a function provided by `embedded-hal`, which involves multiple interactions through GPIO registers. *The HAL crates are available for 14 (32%) MCU families.* Unlike peripheral access crates, HAL crates are not automatically generated but are manually engineered. Consequently, a lot of variance exists in MCU families having HAL crates. For instance, Espressif MCUs (with Xtensa ISA) has HAL crates [114] but does not have peripheral crates.
- **Board Support (BSP):** These crates help in bootstrapping an MCU for an RTOS. Specifically, these included bootloaders and other code to initialize and recognize other hardware peripherals. BSPs are built using HAL and peripheral traits and expose higher-level functions to operate the underlying MCU or System on Chip (SoC). For instance, `hifive1` BSP crate [123] (for HiFive1 boards) exposes a function `configure_spi_flash` which uses `e310x_hal` [122] HAL crate to configure SPI Flash with maximum speed. *There are BSP crates for 19 (44%) different boards.* Unlike peripheral or HAL crates, BSP crates are specific to each board — a combination of MCU and peripherals.

**Open Problem P1.2:** Recent work [128] exposes layering violations in C embedded systems, *i.e.*, components breaking the layered abstraction, *e.g.*, HAL crate not using peripheral crates. However, no such work exists for RUST crates.

- **Driver Crates:** These are device drivers and expose functions to access various aspects of a device. For instance, `eprom24x` driver crate [37] provides the necessary functions (*e.g.*, `read_byte`) to access 24x series serial EEPROMs.
- **RTOS Crates:** These are complete RTOSes, which expose necessary functions for task creation and synchronization, thus enabling easy creation of embedded applications.

**Finding RQ1.1:** Existing hardware support crates mainly target ARM Cortex-M family MCUs and boards. Although, there are ongoing efforts [109] to improve support for other family MCUs such as AVR. It is still a work in progress, and more efforts are required.

**4.1.2 Utility Crates.** These are hardware-independent embedded crates (*i.e.*, `no_std`) that provide various capabilities for embedded systems development. For instance, `tinybmp` embedded crate [43] provides functions to parse BMP images.

Despite the existence of a large number of utility crates in the RUST ecosystem, only 4,764 can be used in embedded systems because of

the requirement to be independent of OS abstractions, *i.e.*, should not use RUST's `std` crate (or be `no_std` compatible). However, it is not easy to convert a crate to be `no_std` [104] compatible as it requires the ability to perform semantic refactoring of the crate and its dependencies. Our Extended Report has an example.

**Open Problem P1.3:** We need techniques to automatically convert RUST crates to be `no_std` compatible to enable existing large quantity of crates to be usable in embedded systems. Recent work by Sharma *et al.*, [127] demonstrates a possible approach using type-based conditional compilation.

**4.1.3 Quality of Embedded RUST.** At a high level, as shown by the last column of Tbl. 1, many (813 (12.69%)) of the crates are just wrappers around C libraries (details in Our Extended Report [126]). We also consider crates that depend on a wrapper crate to be wrapper crates. These crates are susceptible to the bugs in corresponding C libraries. In other words, vulnerabilities in the wrapped libraries can be exploited to get complete control of the corresponding RUST program. This problem has received considerable attention, and several works try to isolate code running as part of libraries (or in general `unsafe` blocks) from the rest of the crate. These techniques depend on special hardware features [20, 58, 67, 80, 106], specifically Intel's Memory Protection Key (MPK) or OS abstractions, such as `mprotect` [10], IPC mechanism [99], sandboxing [72] — making them inapplicable to type-2 embedded systems, *i.e.*, RTOS based embedded systems that run on MCUs.

**Open Problem P1.4:** We need techniques (applicable also for embedded systems) to isolate RUST code from `unsafe` code, *i.e.*, techniques that do not depend on hardware features, OS abstractions, and have low overhead. Recent techniques [65, 66] on C-based embedded software compartmentalization demonstrate possible approaches. However, these should be customized for RUST.

**Code Quality:** We use the following tools to further assess embedded crates' code quality.

- **QRATES [17]:** This tool finds instances of various `unsafe` idioms, *i.e.*, blocks, functions, traits, and trait implementations. Unfortunately, the tool failed on 405 (6.30%) crates. We provide a categorization of failures in Our Extended Report [126]. Out of the remaining 6003 crates, 2634 (43.88%) contain at least one `unsafe` idiom. This is much higher than in non-embedded crates where only 23.6% crates (as reported in [17]) contain `unsafe` idioms. Tbl. 2 shows the results along with top three reasons for `unsafeness`. Note that the percentages are not cumulative, *i.e.*, there could be multiple reasons for an `unsafe` block. These reasons differ from regular crates, indicating the need for different design decisions when creating analysis tools for embedded crates.
- **CLA-METRICS [86]:** Recently, Mergendahl *et al.*, [86] demonstrated the feasibility of Cross-Language-Attacks, wherein interactions of RUST with C/C++ could result in security vulnerabilities. They released CLA-METRICS, a tool to identify these cross-language interaction points. CLA-METRICS works on binaries and require ELF files with specific sections. As we showed in Tbl. 1, there are 813 wrapper crates, which means these contain at least one call from RUST to C/C++, *i.e.*, a transfer point. Interestingly, CLA-METRICS found only 198 crates with interaction points. These results indicate potential issues with the CLA-METRICS tool and

**Table 2: Summary of QRATES results on embedded crates (uf: Call to Unsafe Function, ptr: Dereferencing raw pointer, mstat: Use of Mutable Static, estat: Use of Extern Static, asm: Use of Inline Assembly, union: Access to Union Field)**

Category	Num. Crates Successful (% of Total from Tbl. 1)	Number of Crates (% of Successful) having					Top 3 Reasons for Unsafe Usage
		Unsafe Blocks	Unsafe Functions	Unsafe Trait Impl	Unsafe Trait	At least one unsafe idiom	
RTOS	3 (50.00%)	3 (100.00%)	2 (66.67%)	2 (66.67%)	0 (0.00%)	3 (100.00%)	ud (82%), mstat (29%), estat (6%)
DR	423 (90.77%)	70 (16.55%)	55 (13.00%)	11 (2.60%)	3 (0.71%)	91 (21.51%)	ud (82%), ptr (19%), mstat (2%)
HAL	42 (73.68%)	28 (66.67%)	20 (47.62%)	12 (28.57%)	9 (21.43%)	31 (73.81%)	uf (72%), ptr (38%), mstat (2%)
BSP	89 (78.07%)	25 (28.09%)	23 (25.84%)	4 (4.49%)	0 (0.00%)	29 (32.58%)	uf (88%), ptr (77%), mstat (2%)
PAC	560 (99.12%)	508 (90.71%)	538 (96.07%)	528 (94.29%)	5 (0.89%)	547 (97.68%)	uf (91%), ptr (18%), mstat (1%)
ARCH	10 (66.67%)	9 (90.00%)	10 (100.00%)	1 (10.00%)	1 (10.00%)	10 (100.00%)	uf (52%), asm (31%), ptr (22%)
UTIL	4,473 (93.89%)	1,591 (35.57%)	1053 (23.54%)	554 (12.39%)	210 (4.69%)	1,790 (40.02%)	uf (89%), ptr (16%), union (1%)
UNCAT	403 (95.72%)	102 (25.31%)	78 (19.35%)	30 (7.44%)	8 (1.99%)	133 (33.00%)	uf (84%), ptr (25%), mstat (2%)
<b>Total</b>	6003 (93.68%)	2336 (38.91%)	1779 (29.64%)	1143 (19.04%)	236 (3.93%)	2634 (43.88%)	uf (90%), ptr (18%), mstat (1%)

**Table 3: CLA-METRICS results.**

Category	Num. Crates Successful (% of Total from Tbl. 1)	Num. Crates having at least one Transfer Pt.
RTOS	6 (100.00%)	0 (0.00%)
DR	462 (99.14%)	0 (0.00%)
HAL	56 (98.25%)	0 (0.00%)
BSP	113 (99.12%)	0 (0.00%)
PAC	562 (99.47%)	0 (0.00%)
ARCH	15 (100.00%)	0 (0.00%)
UTIL	4727 (99.22%)	58 (1.23%)
UNCAT	416 (98.81%)	14 (3.37%)
<b>Total</b>	6,357 (99.20%)	73 (1.13%)

we found that an important class of such transfer points that CLA-METRICS misses is indirect function calls. Indirect function calls are common in embedded systems due to their event driven nature. Recent works [30] show that employing CFI mechanism through LLVM can help detect indirect calls. Although as we see in 4.3.3, embedded systems fail to build with clang. We need more work in the area. CLA-METRICS uses the differences in name mangling used by RUST and C++ to determine such transfer points. This information would not be available for indirect function calls and hence CLA-METRICS misses out these.

**Security Implications:** The prevalence of `unsafe` idioms indicates that developers should be more cautious in using embedded crates. Moreover, the robustness issues in analysis tools indicate that security researchers should consider embedded crates as part of their evaluation.

**Finding RQ1.2:** Compared to non-embedded crates, many embedded crates (48.5% v/s 23.6%) contain `unsafe` RUST code idioms.

**Finding RQ1.3:** CLA-METRICS fails to identify cross-language interactions through indirect calls *e.g.*, calls through function pointers.

## 4.2 SAST Tools

As we show in §4.1.3, embedded crates contain a large amount of `unsafe` blocks. The presence of `unsafe` blocks potentially violates RUST’s safety guarantees and results in various memory safety issues [17]. It is important to use SAST tools for embedded systems development in RUST. We investigate the effectiveness of state-of-the-art RUST SAST tools on embedded crates.

**Table 4: Failure reasons of SAST tools and the number of affected crates. Our Extended Report [126] contains detailed and examples of failures.**

Failure Reason	Affected Tools and Crates	Total
Toolchain Incompatibility	FFICHECKER (2559, 39.93%) RUDRA (2547, 39.75%) YUGA (539, 8.41%) SAFEDROP (166, 2.59%) RCANARY (156, 2.43%) LOCKBUD (30, 0.46%)	2,692
Tool Crashes	FFICHECKER (67, 1.046%) RCANARY (9, 0.14%) SAFEDROP (1, 0.015%) LOCKBUD (5, 0.78) YUGA (1, 0.02%)	89
No binary target support	SAFEDROP (27, 0.42%) RCANARY (25, 0.39%) FFICHECKER (6, 0.094%) RUDRA (4, 0.04%)	27
Ignoring Project-Specific Configurations	YUGA (19, 0.30%) RUDRA (6, 0.09%) FFICHECKER (2, 0.03%) SAFEDROP (2, 0.03%) RCANARY (1, 0.02%)	21
Timeouts (large crates)	RCANARY (16, 0.25%)	16
Rustc version incompatibility	FFICHECKER (8, 0.12%)	8
Unknown Reasons	YUGA (7, 0.11%)	7

**4.2.1 Tool Selection.** The recent study by Ami *et al.*, [12] shows that developers are more likely to use SAST tools that do not require any configuration and can be directly used on a software project. Following this, we aim to collect state-of-the-art and readily usable SAST tools. Specifically, these tools should run directly on a given crate and not require any configuration. We searched RUST

**Table 5: Summary of RUST SAST tools evaluated as part of the study and the features that are supported (✓) or not (✗), type of reports (insufficient information, missing relevant details, or detailed report), along with references to results.**

Tool Name	Target Bug Types	Techniques Used	Flow-Tracking				Report Type	Result Reference
			Require	Across Unsafe blocks	Across FFI Boundaries	Handles Async/Indirect Flows		
LOCKBUD [103]	Concurrency, Memory Safety	CallGraph Analysis, Points-to Analysis, Dataflow Analysis	✓	✗	✗	✗	Source Level Traces (Our Extended Report [126])	Refer Our Extended Report [126]
RUDRA [19]	Temporal & Spatial safety	Taint Analysis, Dataflow analysis	✓	✓	✗	✗	Source Level Traces (Our Extended Report [126])	
YUGA [94]	Temporal Safety	Taint Analysis, Alias Analysis	✓	✓	✗	✗	Detailed Source Level Traces (Our Extended Report [126])	
rCANARY [45]	Temporal Safety	Dataflow Analysis, Constraint solving	✓	✓	✗	✗	sat/unsat (Our Extended Report [126])	
FFICHECKER [77]	Temporal Safety	Taint Analysis, Alias Analysis	✓	✓	✓	✗	Generic warning (Our Extended Report [126])	
SAFEDROP [44]	Temporal Safety	Dataflow Analysis, Alias analysis	✓	✓	✗	✗	Affected Function name (Our Extended Report [126])	

forums and the past five years’ proceedings of top-tier security and software engineering conferences and collected the set of readily usable SAST tools. We filtered out tools that did not satisfy our requirements. For instance, we did not select MIRCHECKER [76] because it requires configuring the abstract domain and specifying analysis entry points. After filtering such tools, our investigation resulted in six tools as summarized in Tbl. 5.

Almost all tools except for LOCKBUD focus on identifying temporal safety issues, e.g., incorrect lifetimes, and multiple drops. All these tools are based on flow-tracking as indicated by ✓ under the *Require* column.

**4.2.2 Qualitative Assessment.** As presented in §4.1.3, embedded RUST crates have a higher percentage of unsafe blocks, use FFI functions (i.e., interact with C libraries), and use indirect (or function pointer) calls. SAST tools should be able to handle these idioms to be effective on embedded crates.

**Supported Features:** We referred to the research papers on the corresponding tools and created simple examples to identify their capabilities to handle idioms common in embedded crates. The various columns under *Flow-Tracking* indicate whether each of these idioms is either supported (✓) or not (✗) by the corresponding tools. All tools, except for LOCKBUD, handle flows across **unsafe** blocks. None of the tools handle data-flows through indirect calls (i.e., function pointer calls) – which is one of the common idioms in embedded systems (§2.1 and [128]). Except for FFICHECKER, none of the tools handle flows across FFI boundaries, another common usage in embedded crates.

**Usability:** Despite the existence of standard formats, such as SARIF [3], RUST SAST tools employ ad-hoc ways to report their warnings. As shown in the last column of Tbl. 5, these reports do not always contain the necessary information to triage the underlying defect. The Tbl. 5 also contains references to the examples of corresponding warnings. *All tools, except for YUGA, report their findings in an ad-hoc and hard-to-analyze manner.* rCANARY and FFICHECKER just provide a single-line warning without any details about the source location – which makes these warnings almost impossible to analyze. LOCKBUD, RUDRA, and SAFEDROP provide source level traces. However, the complex semantics of RUST lifetimes make it hard to triage the reported warnings. YUGA provides a well-formatted

HTML report with necessary information about the identified defect.

**4.2.3 Effectiveness.** There is no existing RUST embedded systems bug dataset. The situation is the same for C/C++ [129], which also contains references to the complete results. We evaluated the effectiveness of SAST tools on our embedded crates dataset. The last column of Tbl. 5 has references to the complete results for each tool. **Robustness Issues:** SAST tools fail to handle the diverse build configurations, code structures, and semantics of embedded RUST crates. Consequently, these tools failed on several crates. The Tbl. 4 summarizes different classes of failures, affected tools, and crates. The majority of failures are because of “*Toolchain Incompatibilities*”, i.e., tools fail to identify the backend toolchain required by crates and consequently fail to analyze.

**Precision:** Given the large number of warnings, we used a random sampling method to analyze the precision of the tools. Specifically, we picked 30 crates with more downloads than the median across all the crates. This is to avoid selecting unimportant or rarely used crates.

We ignored rCANARY and FFICHECKER as their warnings did not contain enough information. Furthermore, even for other tools (e.g., LOCKBUD), the information provided is not always sufficient to triage the corresponding warning. We categorized each warning into True Positive (TP), False Positive (FP) or Insufficient Information (IsIn). Tbl. 6 shows the results, the top two reasons for false positives, and the corresponding examples. First, tools were able to find real defects. Our Extended Report [126] shows a real deadlock found by LOCKBUD in the tracing-log crate. However, the true positive rate is very low. Contrary to tools’ claim, all tools suffer from a very high false positive rate (40%-90%) on embedded crates. This is unsurprising as all these tools are evaluated (mostly) on non-embedded crates. This indicates that the design choices of the current tools fail to consider embedded crates.

**Security Implications:** Our results indicate that developers cannot solely rely on existing automated SAST tools to assess their crates and should also perform manual or semi-automated assessments.

### 4.3 C to RUST Conversion Tools

We selected C to RUST conversion tools by following the same approach as for SAST tools (§4.2.1). Although several tools satisfy

**Table 6: Summary of manual analysis of results of various RUST SAST tools with True Positives (TP), False positives (FP), and Insufficient Information (IsIn). We list the top two reasons for FPs here (examples and complete results in our Extended Report [126]). Details in §4.2.3.**

Tool Name	Analysis Results			Top 2 FP Reasons
	TP	FP	IsIn	
LOCKBUD	10 (33%)	14 (46%)	6 (20%)	Lock type ambiguity (42%) Complex Program Semantics (32%)
RUDRA	1 (2.7%)	36 (97.2%)	N/A	Ignoring Explicit Guards (50%) Ignoring Atomic Types (30%)
YUGA	10 (33%)	13 (43%)	7 (23.3%)	Ignoring Caller Contexts (56%) Complex Program Semantics (30%)
SAFEDROP	9 (30%)	17 (56.6%)	4 (13.3%)	Infeasible Paths (80%) Analysis Imprecision (10%)

**Finding RQ1.4:** Current SAST tools lack the necessary features required to effectively handle embedded crates.

**Finding RQ1.5:** Current SAST tools do not provide the necessary information to triage the reported defects, making it hard (rather impossible) to verify the reports.

**Finding RQ1.6:** Current SAST tools fail to effectively handle build idioms and configurations of embedded RUST crates, resulting in robustness issues.

**Finding RQ1.7:** The design choices of current SAST tools fail to effectively handle the common idioms in embedded crates resulting in a very high false positive rate (40%-90%).

**Open Problem P1.5:** There is no dataset of security bugs in RUST embedded crates. Recent systematic bug dataset creation works [59] provide possible approaches to tackle this.

our requirements, we present the results of only the c2RUST tool. Other recent tools, such as LAERTES [50] and CRUSTS [78], do not work directly on C code but rather improve the RUST code produced by c2RUST through novel post-processing techniques. As we will show in §4.3.3, c2RUST either failed or produced uncomparable RUST code on (almost) the entire dataset. Consequently, recent tools that depend on c2RUST also failed on the dataset.

**4.3.1 Dataset.** We collected popular C/C++ based RTOS from osrtos.com, which maintains the list of all popular RTOSes released to date. We selected well-maintained (*i.e.*, has build instructions) and comparable RTOSes. This resulted in a total of 16 C/C++ RTOSes (CRT). The compilation of RTOSes is specific to an MCU and includes HAL and other peripheral access libraries for the MCU. Thus, using RTOSes enables us to test the effectiveness of c2RUST on codebases across different layers of embedded systems.

**4.3.2 Running c2RUST.** To convert a project, we first need to capture compilation commands, *e.g.*, generating `compile_commands.json` using `scan-build` [81]. Next, we need to run c2RUST on the captured `compile_commands.json`. c2RUST uses CLANG to parse C files and uses pattern-based techniques on the resulting Abstract Syntax Tree (AST) to produce corresponding RUST code. Specifically, each compilation command (from `compile_commands.json`) will be executed by replacing the compiler with CLANG. However, just replacing the compiler will not work as embedded systems use

non-standard and MCU specific toolchains, *e.g.*, `avr-gcc`, whose compiler flags/options may not be supported by CLANG. We followed an on-demand approach to convert into a CLANG compatible variant and run c2RUST. Specifically, for each incompatible option leading to an error in conversion/compilation, we refer to CLANG's documentation to see if there is an alternative option (case-1), or if it is not supported by CLANG (case-2). For case-1, we use the corresponding alternative flags, *e.g.*, we replace `-march=nehalem` with `-march=armv8-a`. For case-2, we remove those flags/options (5 flags), *e.g.*, `-Wformat-overflow`. The removal of case-2 flags does not affect the conversion (a frontend task), as all of these flags are related to optimization (a middle/backend task).

**4.3.3 Results.** Our Extended Report [126] has a summary of the results. All RTOSes, except for two, required manually fixing `compile_commands.json` (discussed in §4.3.2). c2RUST failed on 6 (37.5%) RTOSes. The two main reasons for this are: (i) Embedded system codebases often use (CLANG) unsupported C language features, and (ii) c2RUST uses RUST std library to generate certain wrapper functions, but as mentioned in §2.2, std library should not be used in an embedded environment. For instance, `gnuco/oscore.c` file in `stateos/StateOS` uses parameter references in naked functions, which is not supported by CLANG [82] and consequently, c2RUST fails. It executed successfully on 10 (62.5%) RTOSes. Out of which, *the generated RUST code was incorrect or syntactically invalid (e.g., missing semicolon) on 9 (90%) RTOSes*. The conversion was successful (*i.e.*, c2RUST produced comparable RUST code) on only one RTOS, *i.e.*, `kmilo17pet/QuarkTS`.

Finally, c2RUST uses a syntactic approach and consequently produces RUST code with mostly `unsafe` blocks. Although recent works [50] have tried to improve the situation, the progress is rather slow and requires more focused efforts.

**Finding RQ1.8:** C to RUST tools fail on most, *i.e.*, 93.8% (15/16), embedded codebases because of the prevalent use of special compiler flags and non-standard C language features.

**Finding RQ1.9:** C to RUST tools do not consider the `no_std` requirement and consequently will generate RUST code inapplicable for embedded systems.

## 5 RQ2: Interoperability of Rust

Most existing embedded system codebases are written in C [129]. Developers should be able to write RUST code that can interoperate with existing C code to avoid reengineering the entire embedded software stack in RUST. As mentioned in §2.2, RUST has Foreign Function Interface (FFI) support enabling interoperability with code written in other languages, especially C.

To answer this research question, we investigate the effort and challenges in developing RUST (or C) code that can interoperate with C (or RUST) code. We first provide a brief overview of recommended steps to develop interoperable code and quantify the effort and challenges specific to embedded systems. Second, we will present our experience and challenges in engineering interoperable code in various embedded system development scenarios.

### 5.1 RUST $\Leftrightarrow$ C

The top part of Tbl. 7 summarizes our observations.



**Table 7: Summary of Rust Interoperability Modes.** We indicate whether each step is easy (😊), (e.g., running a tool on a C file), requires medium effort or *automation opportunities* (😬) (e.g., configuring linker script), or requires significant effort or *open-problems* (😞) (e.g., rewriting embedded C code in RUST). The challenges affecting embedded systems are **highlighted**.

Interoperability Modes			Method	Effort	Embedded System Specific Challenges
Mode	Sub Abbr.	Desc.			
R ↔ C (§5.1)	R → C (§5.1.1)	Calling C function from Rust	1. Use bindgen to get declaration in Rust (😊) 2. Link with target C object file (😬)	Easy (All C types are FFI Compatible)	N/A
	C → R (§5.1.2)	Calling Rust function from C	1. Use cbindgen to get declaration in C (😬) 2. Link with target Rust object file (😬)	Depends on the use of Rust types not compatible with C types. (i.e., FFI Incompatible types)	Most functions in Embedded crates use FFI incompatible types.
Interoperability in Embedded System Components (§5.2)	RoC (§5.2.2)	Developing Rust Application on top of C-RTOS	1. Use bindgen to get C-RTOS functions' declarations in Rust (😊) 2. Modify the linker script (😬)	Easy (All C type are FFI compatible)	N/A
	CoR (§5.2.3)	Developing C Application on top of Rust RTOS	1. Use cbindgen to get Rust-RTOS functions' declarations in C (😬) 2. Modify the linker script (😬)	Depends on the use of FFI incompatible types in Rust RTOSes.	There is a prevalent use of FFI incompatible types in Rust RTOSes.
	RwC (§5.2.4)	Converting a component in C-RTOS to Rust	1. Use bindgen to convert all dependent component C headers to Rust (😊) 2. Rewrite the target embedded component in Rust (😬) 3. Modify the Makefile (😬)	Depends on the effort to rewrite C code to Rust.	C to RUST conversion tools fail to handle embedded codebases, forcing manual rewriting.

**5.1.1 Calling C function from RUST (RUST → C).** To invoke a C function from from RUST, first, we need to provide the RUST FFI signature of the function. This can be done using tools such as `bindgen` [24] to automatically generate FFI signatures from C header files. Then, they can link the library (i.e., object file) containing the C function with the RUST object file to get the final executable. We illustrate these steps with an example in Our Extended Report [126]. One of the main tasks here is to generate FFI bindings for the C functions. It is relatively straightforward to create these bindings as the RUST's type system [84] is a superset of C's, i.e., every builtin C type has a corresponding type in RUST. Finally, the target object file created from RUST code should be linked to the source C project. However, there are no automated tools to achieve this. In summary, it is relatively straightforward to write RUST code that can invoke C functions, but automation opportunities exist.

**5.1.2 Calling RUST function from C (C → RUST).** Similar to RUST → C (§5.1.1), here we need to generate C declaration for the target RUST function, which can be automated using `cbindgen` [87] tool (Our Extended Report [126] provides details of this process). The superior RUST type system has several types that are not supported in C. For instance, `Vec` [117], one of the most commonly used RUST types, is not supported in C. Consequently, `cbindgen` fails for such functions. Developers need to write type wrappers to handle this manually. But advanced features of RUST types, such as `trait` [137], makes engineering these wrapper functions challenging [61], more details in Our Extended Report [126]. We also performed a type compatibility analysis to assess the extent to which external functions in RUST crates use advanced RUST types, i.e., library functions for which developers need to engineer corresponding type wrapper functions manually. Our Extended Report [126] provides details of the same. This is also the difficulty faced by developers (RQ3.4) as we discuss in §6.3.

**Finding RQ2.1:** Although it is relatively straightforward to invoke C functions from RUST code, automation opportunities exist to ease the process.

**Finding RQ2.2:** The use of FFI incompatible types makes it hard to invoke RUST functions from C code. The majority (~70%) of RUST embedded crates have functions with incompatible types.

**Open Problem P2.1:** Embedding rust function calls in C application is challenging due to the need for type conversion between C types and FFI-incompatible rust types. One possible approach is to manually create (once for all) type wrappers for basic complex types (e.g., `Vec`) and use them to automatically create wrappers for composite types (e.g., `struct`).

## 5.2 RUST Interoperable Challenges in Embedded Systems Development

We used RUST in various real-world scenarios to investigate this aspect. Specifically, we explore: RUST application on top of C RTOS (RoC), C application on top of RUST RTOS (CoR) and converting a component in C RTOS to RUST (RwC). The bottom part of Tbl. 7 summarizes our observations.

**5.2.1 Setup.** We chose the blinker application [95] for our application scenarios (RoC and CoR) as it encompasses all the necessary aspects of a typical embedded system, i.e., interacts with RTOS, has event-driven custom interrupt handler, and uses call-backs. The application periodically (through an interrupt handler) blinks an LED by interacting through GPIO addresses. We used the `nrf52840-dk` MCU board [96] with ARM Cortex-M4 for our target board, as it is a widely recognized and adopted development platform in the embedded systems community and is well-supported by RUST. We used `FREERTOS` [52] as our C RTOSes, because of its widespread popularity in the embedded systems community [141] and extensive

documentation [53]. As mentioned in §3.1, RUST RTOS can be either fully developed in RUST (*i.e.*, native) or wrappers around a C RTOS. We selected LILOS [23] and FREERTOS.RS [54] as our native and wrapper RTOSes, respectively. LILOS is a stable and purely RUST based and completely asynchronous RTOS. This is a representative RUST based RTOS using the strongly suggested async design pattern [85].

**5.2.2 RUST application on top of C RTOS (RoC).** Our goal is to create a RUST blinky application on top of C FREERTOS. We followed similar steps as described in §5.1.1. First, we generated embedded system compatible (*i.e.*, no\_std) RUST FFI bindings from FREERTOS header files using bindgen. Second, we developed blinky application using these FFI bindings. Our Extended Report [126] shows a snippet of creating a task using FREERTOS through its FFI bindings. Specifically, we converted RUST types into appropriate FFI types and invoked the target function. We followed a similar procedure for all other steps, *i.e.*, registering interrupts, etc. Finally, we created a static library of C FREERTOS and linked it with our RUST application to get the final executable. We tested the final executable and ensured that it worked as expected. *The entire process was straightforward.* The only issue was creating a linker script suitable for the target board. As mentioned before in §5.1.2, the availability of automated tools will make this process easier.

#### Listing 1: FFI incompatible function and FFI-friendly wrapper function to create tasks in Lilos scheduler

```

1 // FFI incompatible function
2 pub fn run_tasks(
3     futures: &mut [Pin<&mut dyn Future<Output = Infallible>>],
4     initial_mask: usize,
5 ) -> !
6
7 #[no_mangle]
8 pub extern "C" fn lilos_run_two_tasks(fn1: *mut fn(), fn2: *mut
9     fn(), initial_mask: usize) -> ! {
10     unsafe {
11         let fut1 = *fn1;
12         let future1 = pin!(async move {
13             loop { fut1() } });
14         let fut2 = *fn2;
15         let future2 = pin!(async move {
16             loop { fut2() } });
17         run_tasks(&mut [future1, future2], initial_mask);
18     }

```

**5.2.3 C application on top of RUST RTOS (CoR).** This interoperable modality is crucial for developers who seek to build secure systems by leveraging existing components. Furthermore, as shown in Fig. 4, 36% of developers claim to have developed C code calling RUST functions. Here, our goal is to create a C blinky application on top of RUST RTOSes, specifically on FREERTOS.RS (RUST wrapper of C FREERTOS) and LILOS (a pure RUST RTOS). We followed similar steps as described in §5.1.2.

- **On FREERTOS.RS:** Being a wrapper, all external functions used C compatible types, and cbindgen was able to create C declarations for all the required functions. This made it easy to create the main task of the C blinky application. However, accessing GPIO pins required us to use nrf52840\_pac [116] RUST crate, which uses a C incompatible type, *i.e.*, RegisterBlock. Consequently, cbingen failed to create corresponding C declarations. We manually created an FFI compatible RUST function (togglePin) to access

GPIO pins and used it in our application. Refer our Extended Report [126] for details.

- **On LILOS:** This presented an extreme case wherein none of the external functions are FFI compatible, and consequently, cbindgen failed to create C declarations. We had to manually create FFI compatible wrapper functions (*e.g.*, lilos\_run\_two\_tasks for run\_tasks in Lis. 1). For accessing GPIO pins, we followed the same approach as described before in On FREERTOS.RS.

The main challenge in both cases was dealing with incompatible RUST types. We found (from our analysis in §5.1.2) that on average of 26 interface functions in RUST RTOSes use incompatible RUST types.

**Finding RQ2.3:** Significant development effort is required to engineer a C-embedded application on top of RUST RTOSes because of the prevalent use of incompatible RUST types.

**5.2.4 RUST component in C RTOS (RwC).** Here, we aim to convert a component in C RTOS into RUST to mimic an incremental porting scenario. We selected list component in C FREERTOS, as it is self-contained (*i.e.*, no calls to other components). We followed a similar procedure as described in §5.1.1. First, we used bindgen on list.h to create the required RUST types. The xLIST (in our Extended Report [126]) shows the type generated by bindgen. Second, we reimplemented the list functions (in list.rs) using the types generated by bindgen. Unfortunately, as mentioned in §4.3, the recommended way to convert C to RUST code does not work on embedded codebases. We manually translated the corresponding C implementation line-by-line into RUST, which required considerable effort. Our Extended Report shows a snippet of vListInitialise function in RUST. Finally, we modified the Makefile to build list.rs into a static library and linked it with the final FREERTOS object file.

**Finding RQ2.4:** The lack of embedded codebase support in C-to-RUST conversion tools (described in §4.3) poses a considerable challenge in adopting the (recommended) incremental porting approach [50, 134] to convert embedded codebases to RUST.

## 6 RQ3: Developers Perspective

We aim to shed light on developers' perspectives on using RUST for embedded systems development. Specifically, (i) Reasons for not using RUST.; (ii) Challenges faced by developers in using RUST.; and (iii) Developer's perspective on RUST's performance, safety and interoperability.

### 6.1 Study Methodology

We used an anonymous online survey with questions spanning various categories as shown in Tbl. 8. We recruited participants by sending the link to our survey to various embedded systems communities and RUST embedded developers' mailing lists (Details in Our Extended Report [126]). Also, we used our industry collaborations to circulate our survey to multiple organizations. Our Institutional Review Board (IRB) reviewed and approved our study protocol.

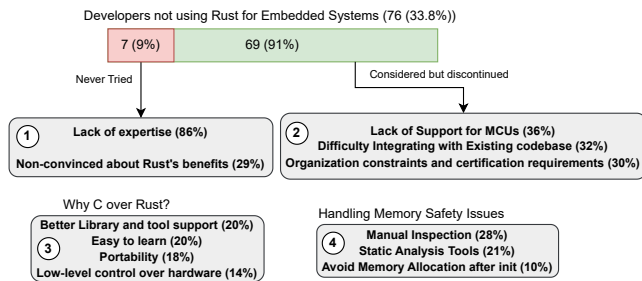
**Survey Respondents.** We got 268 responses, out of which we filtered out 43 responses from inattentive participants (through

**Table 8: Summary of Survey Questions. Exact questions in Our Extended Report [126]**

Category	Description	No. of questions
Familiarity and Experience	Examines participants' familiarity, experience, and preferred languages for embedded systems development.	9
Acquaintance with Rust	Explores familiarity with Rust and its specific features of participants.	15
Reasons to Use Rust	Gathers opinions of participants on reasons to use Rust, its advantages, and perceived challenges.	13
Hardware Support, Integration, and Performance	Enquires about issues related to hardware support, integration, and performance when using Rust for embedded development.	11
Memory Safety and Debugging	Focuses on the importance of memory safety, ease of debugging with Rust, and related practices in embedded systems development.	9
Documentation and Community Support	Evaluates the quality of Rust documentation and the level of community support available for embedded systems.	6
Development Time and Code Quality	Investigates views on potential gains in development time and code quality when using Rust.	3

attention-checking questions), resulting in 225 valid responses. There is considerable diversity in the embedded systems experience of participants, indicating a representative developers group. Our Extended Report shows the distribution of embedded systems experience of participants.

## 6.2 Not Using RUST for Embedded Systems: Expectations v/s Reality



**Figure 1: Response summary of Developers not using RUST.**

The Fig. 1 shows the summary of 76 (33.8%) participants who currently do not use RUST. Only, 7 (9%) participants never tried to use RUST, mainly because of the lack expertise (①). Furthermore, 29% of developers are not convinced about RUST security benefits as *embedded systems rarely use dynamic memory allocation and do not need RUST's ownership features — an important safety feature of RUST.*

However, the other 69 (91%) participants considered RUST, but discontinued because of three main reasons (②): (i) Lack of support for MCUs, this is inline with our analysis in §4. (ii) Integrating with existing codebases. (iii) Organizational and certification constraints. Source code used as part of critical infrastructure, such as airplanes, undergo rigorous certification [47, 69, 70]. This is expensive and time-consuming. Switching to RUST requires re-certification, which may not be desirable for organizations.

All developers in Fig. 1 use C, and the ③ box shows the reasons for choosing C. The first two reasons are expected, as C is an old language with many libraries and toolchain support. The third reason, *i.e.*, Portability, is interesting. In C, there are no language-specific considerations for embedded systems. Consequently, it is relatively easy to port (or repurpose) an existing library for the embedded use case by linking it with embedded versions of standard

libraries. However, in RUST, *embedded libraries (i.e., crates) should be developed with no\_std environment — which restricts the uses of certain language-level features. Consequently, porting existing libraries to be no\_std compatible and to use in embedded systems is challenging [40, 46].*

Interestingly, as shown in ④ of Fig. 1, many (28%) embedded systems developers (using C) do not use any automated security tools and rely on manual inspection. Only 21% of the developers use static analysis tools. This confirms observations made by a recent study [129]. Finally, none of the developers use any dynamic analysis tools.

**Finding RQ3.1:** To improve adoption of RUST for embedded systems:

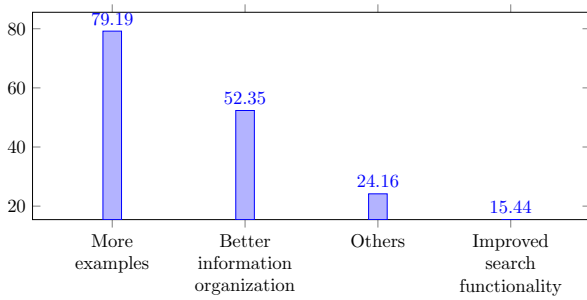
- Support needs to be added for more MCUs.
- Techniques and methods should be developed to ease the certification of RUST code ported from already certified C code.
- Automated techniques should be developed to convert RUST crates to no\_std compatible.

## 6.3 Experiences in Using RUST for Embedded Systems

There were 149 (66.2%) participants who currently use RUST for embedded systems development. These participants have varied development experience with RUST, specifically, 19% with < 6 months, 28% with 6 months - 1 year and 18% with 1-2 years, and 35% with more than 2 years.

**Adopting RUST and Motivation:** The two main motivations to learn RUST for embedded systems are safety and reliability (94%) and familiarity with the language (57%). Although there exists good support for RUST in the embedded systems community (Fig. 7), the majority percentage (85%) of developers claim that it still requires considerable effort (*i.e.*, Moderate (40%) + Hard (45%)) to adopt RUST. **RUST Documentation and Community Support (Fig. 6 and 2):** The majority, *i.e.*, 81% (51 + 30) of developers, agree that the available documentation and community are helpful. However, 49% (30 + 19) of developers mention that documentation should be improved. The Fig. 2 shows specific suggestions to improve the documentation. Specifically, *RUST documentation should contain more examples and be organized better.*

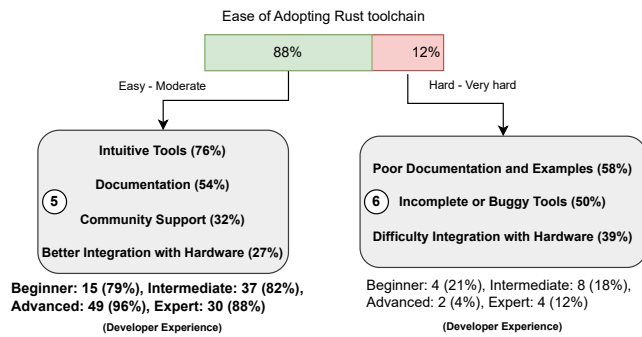
**Developer Tools and Crates:** 68% of the developers think the currently available crates provide sufficient support (*i.e.*, very satisfied – somewhat satisfied), whereas the rest, 32%, *observe that it is not*



**Figure 2: Required Improvements to Rust Documentation.**

adequate (i.e., dissatisfied – very dissatisfied). This is also in line with our analysis (§4), where we noticed that necessary support (i.e., HAL and other necessary crates) is unavailable for certain MCUs.

Fig. 3 shows the opinion of users w.r.t the RUST toolchain support. Most developers (across all experience levels) mention that it is easy to adopt RUST toolchain for embedded systems development – reasons are intuitive tools and their documentation (⑤). This is in line with our analysis in §5. Nonetheless, 12% (18) developers expressed concerns, i.e., poor documentation (missing examples) and buggy tools (⑥). These could be because of using tools from non-stable branches. This also further confirms observations in Fig. 2, where developers require more examples to be included in the documentation.



**Figure 3: Response summary of Developers on ease of adopting RUST toolchain.**

**Performance of RUST:** It is interesting to see that only 54% of developers mentioned that they performed a systematic comparative evaluation of their RUST implementation with existing C implementation. Wherein 28.5% noticed similar performance, 22% noticed that RUST was faster, and the remaining 3.5% noticed that RUST implementation was slower.

The slowdown observations contradict the common belief that given the asynchronous nature of embedded systems, the performance of RUST’s implementation can be significantly improved by carefully using its built-in features, such as closures [60]. These observations also highlight the need for a systematic performance evaluation of using RUST for embedded systems.

**Finding RQ3.2:** RUST documentation should be improved with more embedded system-specific examples.

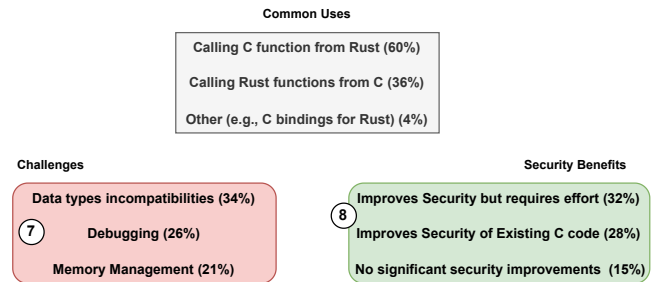
**Open Problem P3.1:** Developers have contradictory views on RUST’s performance on embedded systems. Existing performance studies [21, 71, 144] could be extended to include RUST embedded systems.

**Interoperability with Existing Codebase:** All developers agree that interoperability is needed, and most developers (98%) were aware of RUST’s interoperability support. However, 56% of developers mentioned that they face challenges in using interoperability support of RUST. The ⑦ box in Fig. 4 shows developers’ common challenges in using interoperability support.

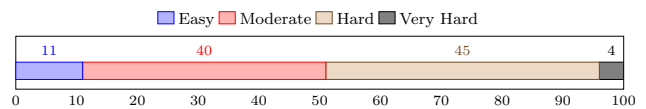
**Finding RQ3.3:** The majority (i.e., 34%) of developers face issues handling type incompatibilities between RUST and C code. This is in line with our analysis (§5.1.2), where we show that handling data types is one of the challenges in using C on top of RUST (CoR).

The second major (26%) issue is debugging, which is expected because, as explained in §2, embedded systems follow an asynchronous and event-driven design. This results in frequent cross-language domain interactions and makes debugging hard.

As shown by ⑧ in Fig. 4, 60% (32 + 28) of developers agree that using interoperable RUST improves security. However, 32% mention that secure usage (i.e., through unsafe blocks) requires significant effort – which is in line with existing works [17] that show that engineering interoperable code in unsafe blocks is challenging and prone to security issues.



**Figure 4: Response summary of Developers perspectives on RUST’s Interoperability.**



**Figure 5: Ease of Adopting Rust for Embedded Systems Development**

**RUST v/c C:** 92% of developers mentioned that they also used C for embedded systems development. Out of which, 64% of developers claim that development time significantly decreased and also the code quality improved after switching to RUST. This is in line with recent findings at Google [31]. For embedded systems development, 30% of developers recommend RUST unconditionally, whereas 61% recommend RUST only if the developer is well-versed in it, and the 9% recommend RUST only if safety is of high importance.



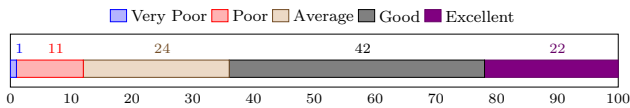


Figure 6: Support for Rust in Embedded Systems Community

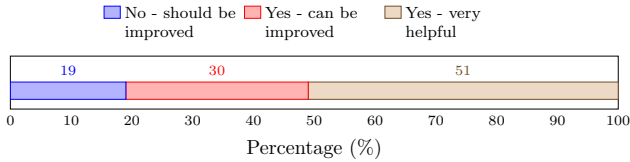


Figure 7: Is Rust Documentation and Community Helpful?

## 7 Limitations and Threats to Validity

We acknowledge the following limitations and threats to validity:

- Our findings are based on the analysis of the collected dataset and SAST tools. The dataset and the tools may not be representative enough. We tried to avoid this by collecting crates and tools from diverse sources.
- We did not analyze all the alerts raised by various SAST tools (§4.2). Consequently, using these alerts to assess the quality of crates could be exaggerated because of potential false positives.
- Our RQ2 (§5.2) observations are based on limited scenarios and may not be generalizable. However, the developer survey (in §6.3) confirmed our findings, reducing the risk.

## 8 Related Work

**Rust Studies:** Several works [17, 55, 88, 103, 145] evaluate various aspects of RUST from the usability perspective. Fulton *et al.*, [55] surveyed and interviewed RUST developers to understand challenges and barriers to adoption. Similarly, Zeng *et al.*, [145] performed a mixed-methods study of RUST related forums to identify common challenges and corresponding solutions. However, other works assess specific aspects of RUST. Astrauskas *et al.*, [17] focused on identifying common uses of `unsafe` blocks. Whereas, Qin *et al.*, [103] focused on identifying challenges in using concurrency constructs and identified common causes of concurrency issues in RUST code. Similarly, Mindermann *et al.*, [88] exclusively studied the usability of RUST's cryptography APIs, providing crucial recommendations for developing these APIs to enhance usability and reduce misuse.

Pinho *et al.*, [100] and Ashmore *et al.*, [16] evaluated the feasibility of using RUST for safety critical systems (a class of embedded systems). Specifically, using evaluation criteria for programming languages, aligning with the standards set by RTCA DO-178C, they demonstrated that RUST meets all the criteria. Levy *et al.*, [74] focused on using RUST for kernel development and shared their first-hand experience in creating a kernel for low-power MCUs. They also demonstrated [73, 75] the feasibility of using RUST to engineer common kernel building blocks with only a few `unsafe` blocks. This paper assesses the applicability and challenges of using RUST for embedded system software, such as RTOSes, by performing a systematic analysis and developer study.

**Embedded Systems Vulnerabilities:** Several works [49, 51, 57] try to understand vulnerabilities in embedded systems and analyze challenges and possible solutions for effective vulnerability detection. Several embedded systems vulnerability detection techniques

use various approaches ranging from static analysis [105], symbolic execution [33], and rehosting-based dynamic analysis [125] or fuzzing [29]. In this work, we do not propose any new techniques but rather use state-of-the-art tools (§4.2) to assess various aspects of RUST embedded software.

## 9 Conclusion

We performed a systematic analysis and a comprehensive (with 225 developers) survey to understand the current state and challenges in using RUST for embedded systems development. Our findings provide insights into the current state and expose open problems and potential improvements that can facilitate easy adoption of RUST for embedded system development.

## 10 Acknowledgements

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